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MOS FET Power Amplifier Module for E-GSM and DCS1800/1900 Triple Band Handy Phone



ADE-208-1606 (Z)

Rev.0 Oct. 2002

Application

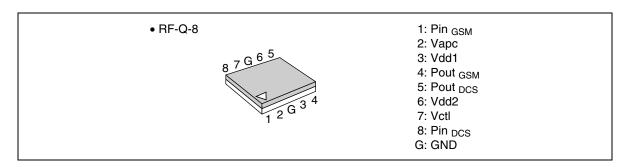
- Triple band amplifier for E-GSM (880 MHz to 915 MHz), DCS1800/1900 (1710 MHz to 1785 MHz, 1850 MHz to 1910 MHz).
- For 3.5 V & GPRS Class12 operation compatible

Features

- All in one including output matching circuit
- Simple external circuit
- Simple power control
- High gain 3stage amplifier : 0 dBm input Typ
- Lead less thin & Small package : $8.0 \times 10.0 \text{ mm Typ} \times 1.5 \text{ mm Max}$
- High efficiency : 55% Typ at 35.0 dBm for E-GSM

47% Typ at 32.5 dBm for DCS1800 47% Typ at 32.0 dBm for DCS1900

Pin Arrangement



Absolute Maximum Ratings *1

 $(Tc = 25^{\circ}C)$

Item	Symbol	Rating	Unit	Remark	
Supply voltage	Vdd	7.0	V	at no-operation	
		5.0	V	at operation (50 Ω load)	
Supply current	Idd _{GSM}	3.5	А		
	Idd _{DCS}	2	А		
Vctl voltage	Vctl	4	V		
Vapc voltage	Vapc	4	V		
Input power	Pin	10	dBm		
Operating case temperature *2	Tc (op)	-30 to +100	°C		
Storage temperature	Tstg	-40 to +100	°C		
Output power	Pout _{GSM}	5	W		
	Pout DCS	3	W		

Notes: 1. The maximum ratings shall be valid over both the E-GSM-band (880 to 915 MHz), and the DCS1800/1900-band (1710 to 1785 MHz, 1850 to 1910 MHz).

2. These are specified at pulsed operation with pulse width = 1154 μ sec and duty cycle of 2:8.

Electrical Characteristics for DC

 $(Tc = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Drain cutoff current	lds	—	—	20	μA	Vdd = 4.7 V, Vapc = 0 V, Vctl = 0.2 V
Vapc control current	lapc	—		2.0	mA	Vapc = 2.2 V
Vctl control current	lctl	_		2	μA	Vctl = 3 V

Electrical Characteristics for E-GSM band

 $(Tc = 25^{\circ}C)$

Test conditions unless otherwise noted:

f = 880 to 915 MHz, Vdd1 = Vdd2 = 3.5 V, Pin = 0 dBm, Vctl = 2.0 V, Rg = Rl = 50 Ω , Tc = 25°C, Pulse operation with pulse width 1154 μ s and duty cycle 2:8 shall be used.

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Frequency range	f	880	_	915	MHz	
Band select (GSM active)	Vctl	2.0	_	2.8	V	
Input power	Pin	-2	0	2	dBm	
Control voltage range	Vapc	0.2	_	2.2	V	
Supply voltage	Vdd	3.1	3.5	4.5	V	
Total efficiency	η_{T}	47	55	_	%	Pout _{GSM} = 35 dBm,
2nd harmonic distortion	2nd H.D.	_	-15(-50)	0(-35)	dBm(dBc)	Vapc = controlled
3rd harmonic distortion	3rd H.D.	_	-10(-45)	0(-35)	dBm(dBc)	
4th~8th harmonic distortion	4th~8th H.D.	_		0(-35)	dBm(dBc)	
Input VSWR	VSWR (in)	_	1.5	3		
Output power (1)	Pout (1)	35.0	36.0	_	dBm	Vapc = 2.2 V
Output power (2)	Pout (2)	33.5	34.5	—	dBm	Vdd = 3.1 V, Vapc = 2.2 V, Tc = +85°C
ldd at Low power	_		_	300	mA	Pout _{GSM} = 7 dBm
Isolation	_	_	-48	-37	dBm	Vapc = 0.2 V
Isolation at DCS RF-output when GSM is active	_	_	-25	-18	dBm	Pout _{GSM} = 35 dBm, Measured at f = 1760 to 1830 MHz
Switching time	t _r , t _r	_	1	2	μS	Pout _{GSM} = 5 to 35 dBm
Stability	_	No parasitic oscillation > –36 dBm			_	$\label{eq:Vdd} \begin{array}{l} \mbox{Vdd} = 3.1 \mbox{ to } 4.5 \mbox{ V, Pout} \le 35 \mbox{ dBm}, \\ \mbox{Vapc}_{\mbox{\tiny GSM}} \le 2.2 \mbox{ V, Rg} = 50 \Omega, \\ \mbox{Output VSWR} = 6:1 \mbox{ All phase angles} \end{array}$
Load VSWR tolerance	_	No degradation — or Permanent degradation				$\label{eq:Vdd} \begin{array}{l} \mbox{Vdd} = 3.1 \mbox{ to } 4.5 \mbox{ V}, \mbox{Pout}_{\mbox{GSM}} \leq 35 \mbox{ dBm}, \\ \mbox{Vapc}_{\mbox{GSM}} \leq 2.2 \mbox{ V}, \mbox{ Rg} = 50 \ \Omega, \mbox{ t} \leq 20 \mbox{ sec.} \\ \mbox{Output VSWR} = 10 : 1 \mbox{ All phase angles} \end{array}$
Load VSWR tolerance at GPRS CLASS 12 operation		No degradation — or Permanent degradation			_	$\label{eq:states} \begin{array}{l} \mbox{Vdd}=3.1 \mbox{ to } 4.2 \mbox{ V, Pout}_{\mbox{\tiny GSM}} \leq 35 \mbox{ dBm}, \\ \mbox{Vapc}_{\mbox{\tiny GSM}} \leq 2.2 \mbox{ V,} \\ \mbox{Rg}=50 \ \Omega, \ t \leq 20 \ \mbox{sec.}, \ \mbox{Tc} \leq 90^{\circ}\mbox{C}, \\ \mbox{Output VSWR}=10:1 \mbox{ All phase angles} \end{array}$
Slope Pout/Vapc	_	_	160	200	dB/V	Pout _{GSM} = 5 to 35 dBm
AM output	_	—	15	20	%	Pout _{GSM} = 5 to 35 dBm, 4% AM modulation at input 50 kHz modulation frequency

Electrical Characteristics for DCS1800 band

 $(Tc = 25^{\circ}C)$

Test conditions unless otherwise noted:

f = 1710 to 1785 MHz, Vdd1 = Vdd2 = 3.5 V, Pin = 0 dBm, Vctl = 0 V, Rg = Rl = 50 Ω , Tc = 25°C, Pulse operation with pulse width 1154 μ s and duty cycle 2:8 shall be used.

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Frequency range	f	1710		1785	MHz	
Band select (DCS active)	Vctl	0	_	0.1	V	
Input power	Pin	-2	0	2	dBm	
Control voltage range	Vapc	0.2	_	2.2	V	
Supply voltage	Vdd	3.1	3.5	4.5	V	
Total efficiency	η_{T}	40	47	_	%	Pout _{DCS} = 32.5 dBm,
2nd harmonic distortion	2nd H.D.	—	-14.5(-47)	-2.5(-35)	dBm(dBc)	Vapc = controlled
3rd harmonic distortion	3rd H.D.	—	-7.5(-40)	-2.5(-35)	dBm(dBc)	
4th~8th harmonic distortion	4th~8th H.D.	_	_	-2.5(-35)	dBm(dBc)	
Input VSWR	VSWR (in)	_	1.5	3	_	
Output power (1)	Pout (1)	32.5	33.5	_	dBm	Vapc = 2.2 V
Output power (2)	Pout (2)	31.0	32.0	—	dBm	Vdd = 3.1 V, Vapc = 2.2 V, Tc = +85°C,
ldd at Low power	_	_		150	mA	Pout _{DCS} = 5 dBm
Isolation	—	_	-42	-37	dBm	Vapc = 0.2 V
Switching time	t _r , t _r	—	1	2	μs	Pout _{DCS} = 0 to 32.5 dBm
Stability	_	No parasitic oscillation - > –36 dBm			_	$\label{eq:Vdd} \begin{array}{l} Vdd=3.1 \mbox{ to } 4.5 \mbox{ V}, \mbox{ Pout}_{_{DCS}} \leq 32.5 \mbox{ dBm}, \\ Vapc \leq 2.2 \mbox{ V}, \mbox{ Rg}=50 \Omega, \\ Output \mbox{ VSWR}=6:1 \mbox{ All phase angles} \end{array}$
Load VSWR tolerance		No degradation - or Permanent degradation			_	$\label{eq:Vdd} \begin{array}{l} Vdd=3.1 \mbox{ to } 4.5 \mbox{ V}, \mbox{ Pout}_{\mbox{ DCS}} \leq 32.5 \mbox{ dBm}, \\ Vapc \leq 2.2 \mbox{ V}, \mbox{ Rg}=50 \Omega, \mbox{ t} \leq 20 \mbox{ sec.}, \\ Output \mbox{ VSWR}=10:1 \mbox{ All phase angles} \end{array}$
Load VSWR tolerance at GPRS CLASS 12 operation	_	No degradation or Permanent degradation			_	$\label{eq:Vdd} \begin{array}{l} Vdd = 3.1 \mbox{ to } 4.2 \mbox{ V}, \mbox{ Pout}_{_{DOS}} \leq 32.5 \mbox{ dBm}, \\ Vapc \leq 2.2 \mbox{ V}, \\ Rg = 50 \ \Omega, \ t \leq 20 \ sec., \ Tc \leq 90^{\circ}C, \\ Output \ VSWR = 10: 1 \mbox{ All phase angles} \end{array}$
Slope Pout/Vapc	_	_	160	200	dB/V	Pout _{DCS} = 0 to 32.5 dBm
AM output	_	_	15	20	%	Pout _{DOS} = 0 to 32.5 dBm, 4% AM modulation at input 50 kHz modulation frequency

Electrical Characteristics for DCS1900 band

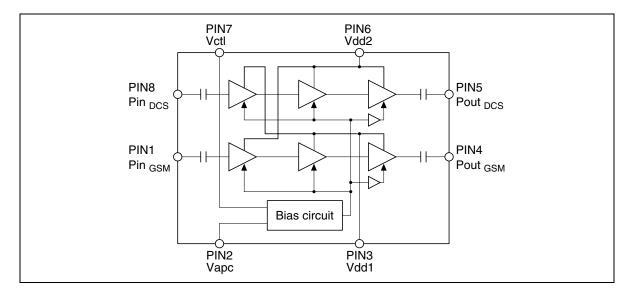
 $(Tc = 25^{\circ}C)$

Test conditions unless otherwise noted:

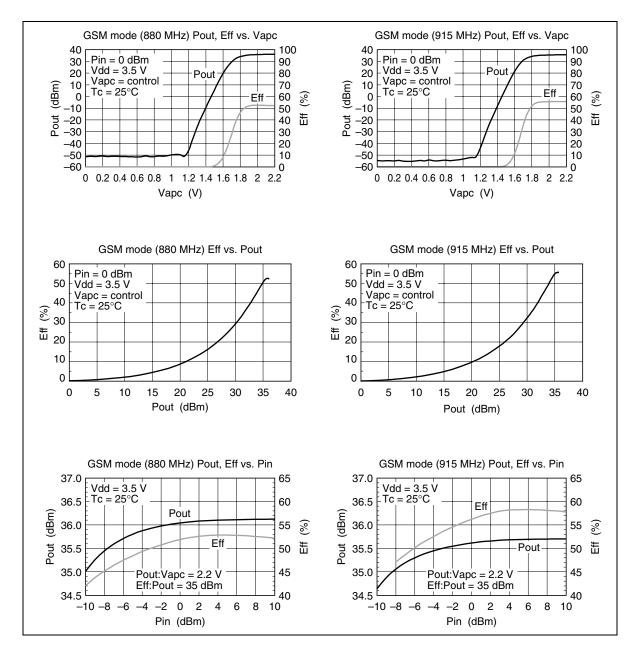
f = 1850 to 1910 MHz, Vdd1 = Vdd2 = 3.5 V, Pin = 0 dBm, Vctl = 0.2 V, Rg = Rl = 50 Ω , Tc = 25°C, Pulse operation with pulse width 1154 μ s and duty cycle 2:8 shall be used.

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Frequency range	f	1850		1910	MHz	
Band select (DCS active)	Vctl	0	_	0.1	V	
Input power	Pin	-2	0	2	dBm	
Control voltage range	Vapc	0.2		2.2	V	
Supply voltage	Vdd	3.1	3.5	4.5	V	
Total efficiency	η_{τ}	40	47		%	Pout _{DCS} = 32.0 dBm,
2nd harmonic distortion	2nd H.D.	—	-15(-47)	-3(-35)	dBm(dBc)	Vapc = controlled
3rd harmonic distortion	3rd H.D.	—	-8(-40)	-3(-35)	dBm(dBc)	
4th~8th harmonic distortion	4th~8th H.D.	_	—	-3(-35)	dBm(dBc)	•
Input VSWR	VSWR (in)	_	1.5	3	—	•
Output power (1)	Pout (1)	32.0	33.0		dBm	Vapc = 2.2 V
Output power (2)	Pout (2)	30.5	31.5	—	dBm	Vdd = 3.1 V, Vapc = 2.2 V, Tc = +85°C
ldd at Low power	_	_	_	150	mA	Pout _{DCS} = 5 dBm
Isolation	_	_	-42	-37	dBm	Vapc = 0.2 V
Switching time	t _r , t _r	—	1	2	μs	Pout _{DCS} = 0 to 32.0 dBm
Stability		No parasitic oscillation > –36 dBm			_	$\label{eq:Vdd} \begin{array}{l} \mbox{Vdd} = 3.1 \mbox{ to } 4.5 \mbox{ V}, \mbox{Pout}_{\mbox{\tiny DCS}} \leq 32.0 \mbox{ dBm} \\ \mbox{Vapc} \leq 2.2 \mbox{ V}, \mbox{ Rg} = 50 \ \Omega, \\ \mbox{Output VSWR} = 6 : 1 \mbox{ All phase angles} \end{array}$
Load VSWR tolerance	—	No degradation — or Permanent degradation			—	$\label{eq:Vdd} \begin{array}{l} \mbox{Vdd} = 3.1 \mbox{ to } 4.5 \mbox{ V}, \mbox{Pout}_{\mbox{\tiny DCS}} \leq 32.0 \mbox{ dBm} \\ \mbox{Vapc} \leq 2.2 \mbox{ V}, \mbox{ Rg} = 50 \ \Omega, \mbox{ t} \leq 20 \mbox{ sec.}, \\ \mbox{Output} \mbox{ VSWR} = 10 : 1 \mbox{ All phase angles} \end{array}$
Load VSWR tolerance at GPRS CLASS 12 operation		No degradation or Permanent degradation			_	$\label{eq:Vdd} \begin{array}{l} \mbox{Vdd} = 3.1 \mbox{ to } 4.2 \mbox{ V}, \mbox{Pout}_{\mbox{\tiny DCS}} \leq 32.0 \mbox{ dBm} \\ \mbox{Vapc} \leq 2.2 \mbox{ V}, \\ \mbox{Rg} = 50 \ \Omega, \ t \leq 20 \ sec., \ Tc \leq 90^{\circ}C, \\ \mbox{Output} \ VSWR = 10: 1 \mbox{ All phase angles} \end{array}$
Slope Pout/Vapc	_	_	160	200	dB/V	Pout _{DCS} = 0 to 32.0 dBm
AM output	_	_	15	20	%	Pout _{DCS} = 0 to 32.0 dBm, 4% AM modulation at input 50 kHz modulation frequency

Circuit Diagram

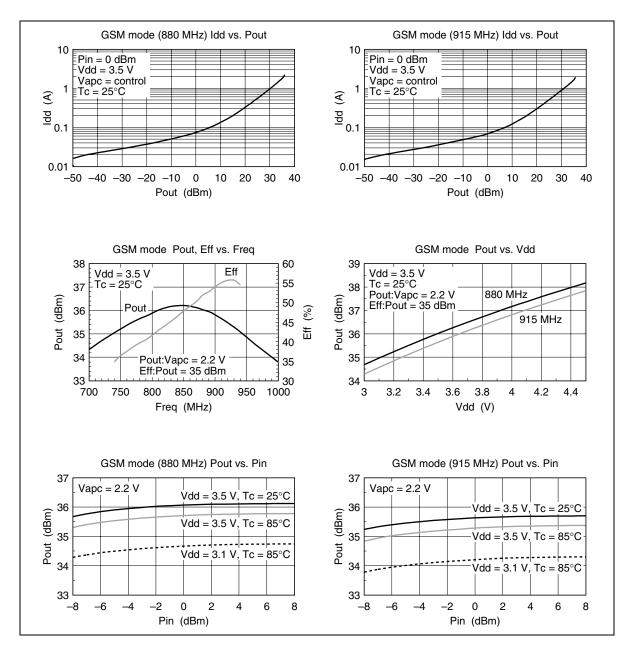


Characteristic Curves

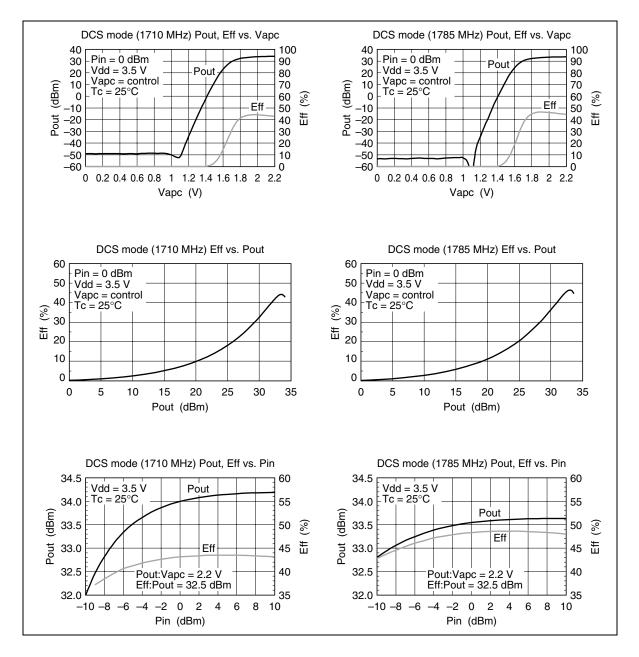


GSM mode (880MHz to 915 MHz)

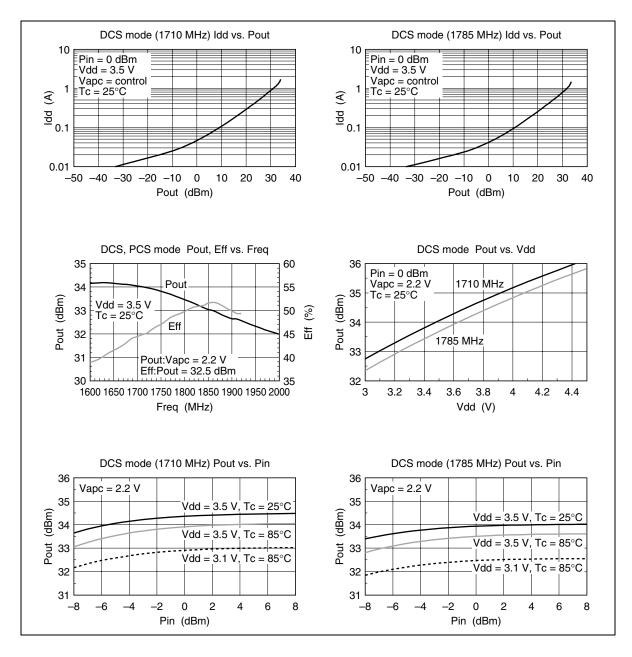
GSM mode (880MHz to 915 MHz) (cont.)



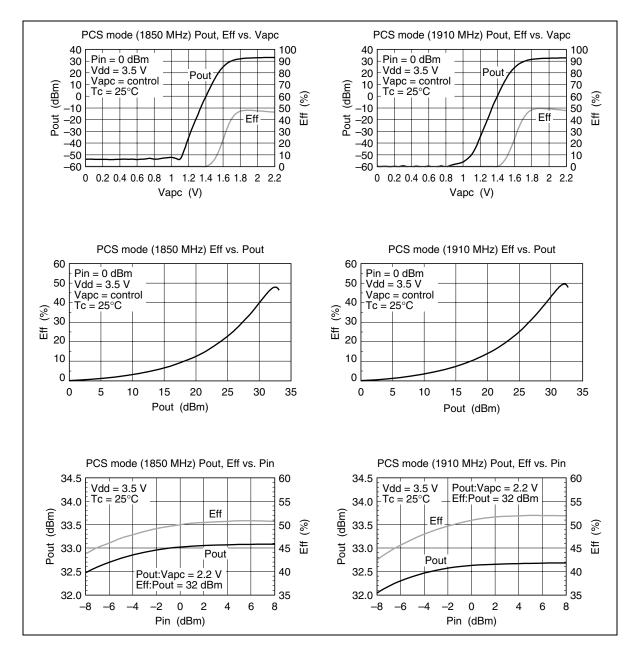
DCS mode (1710MHz to 1785 MHz)



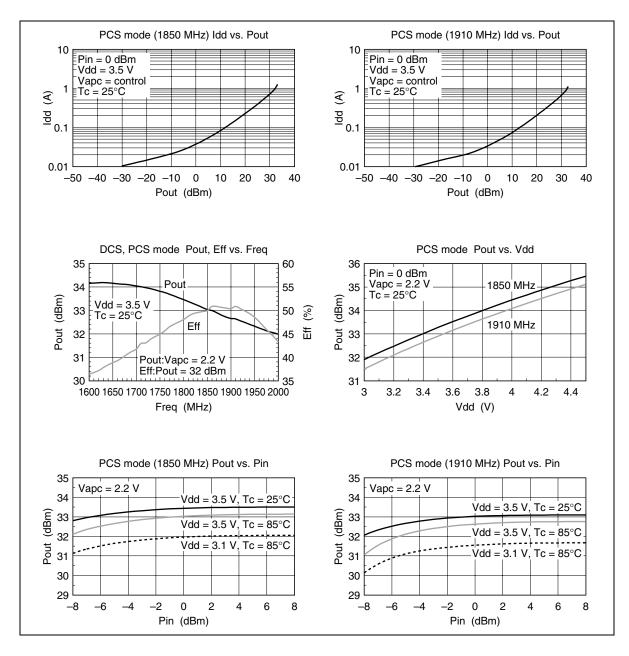
DCS mode (1710MHz to 1785 MHz) (cont.)



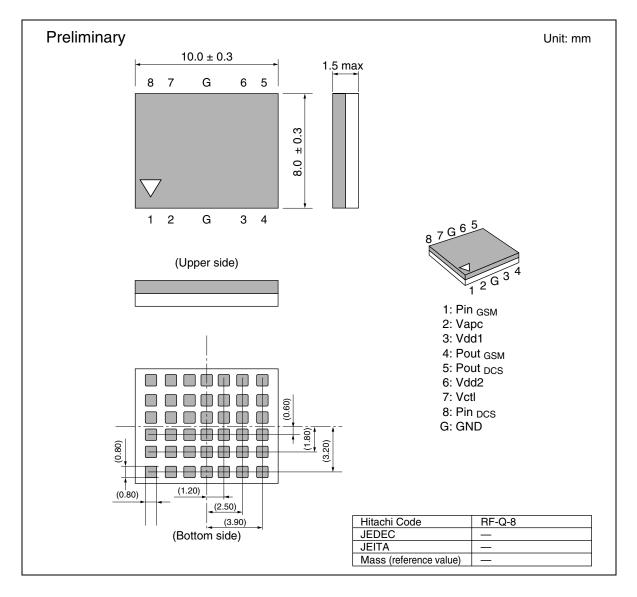
PCS mode (1850MHz to 1910 MHz)



PCS mode (1850MHz to 1910 MHz) (cont.)







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