Small Package, High Performance, Asynchronies Boost for 10 WLED Driver

General Description

The RT9293 is a high frequency, asynchronous boost converter. The internal MOSFET can support up to 10 White LEDs for backlighting and OLED power application, and the internal soft start function can reduce the inrush current. The device operates with 1-MHz fixed switching frequency to allow small external components and to simplify possible EMI problems. Moreover, the IC comes with 46V over voltage protection to allow inexpensive and small-output capacitors with lower voltage ratings. The LED current is initially set with the external sense resistor R_{SET}. The RT9293 is available in the tiny package type TSOT-23-6 and WDFN-8L 2x2 packages to provide the best solution for PCB space saving and total BOM cost.

Ordering Information

RT9293□□□

- Package Type
  - J6 : TSOT-23-6
  - QW : WDFN-8L 2x2 (W-Type)
- Operating Temperature Range
  - G : Green (Halogen Free with Commercial Standard)
- Feedback Voltage Reference
  - A : 104mV
  - B : 300mV
  - C : 200mV

Note :

Richtek Green products are :

- RoHS compliant and compatible with the current requirements of IPC/JEDEC J-STD-020.
- Suitable for use in SnPb or Pb-free soldering processes.
- 100% matte tin (Sn) plating.

Marking Information

For marking information, contact our sales representative directly or through a Richtek distributor located in your area, otherwise visit our website for detail.

Features

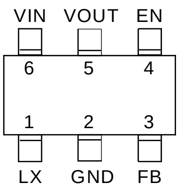
- **VIN Operating Range : 2.5V to 5.5V**
- **Internal Power N-MOSFET Switch**
- **Wide Range for PWM Dimming (100Hz to200kHz)**
- **Minimize the External Component Counts**
- **Internal Soft Start**
- **Internal Compensation**
- **Under Voltage Protection**
- **Over Voltage Protection**
- **Over Temperature Protection**
- **Small TSOT-23-6 and 8-Lead WDFN Packages**
- **RoHS Compliant and Halogen Free**

Applications

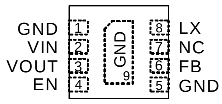
- Cellular Phones
- Digital Cameras
- PDAs and Smart Phones and MP3 and OLED.
- Probable Instruments

Pin Configurations

(TOP VIEW)



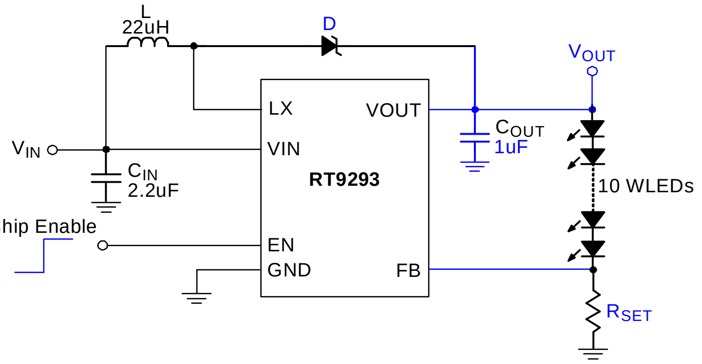
TSOT-23-6



WDFN-8L 2x2

Note : There is no pin1 indicator on top mark for TSOT-23-6 type, and pin 1 will be lower left pin when reading top mark from left to right.

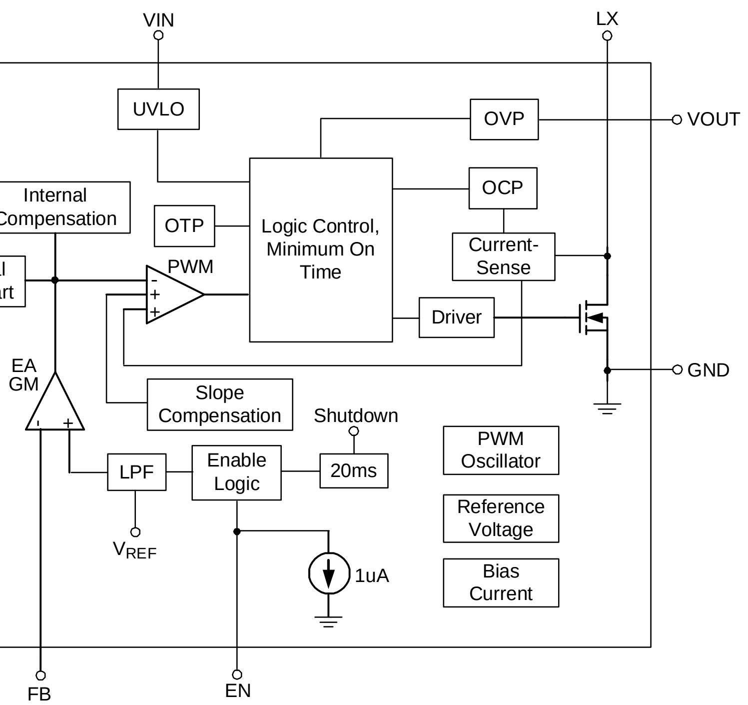
Typical Application Circuit



Functional Pin Description

Pin No.		Pin Name	Pin Function
RT9293□GJ6	RT9293□GQW		
1	8	LX	Switching Pin.
2	1, 5, Exposed pad (9)	GND	Ground Pin. The exposed pad must be soldered to a large PCB and connected to GND for maximum power dissipation.
3	6	FB	Feed Back Pin, put a resistor to GND to setting the current.
4	4	EN	Chip Enable (Active High).
5	3	VOUT	Output Voltage Pin.
6	2	VIN	Input Supply.
--	7	NC	No Internal Condition.

Function Block Diagram



Absolute Maximum Ratings (Note 1)

- Supply Input Voltage, V_{IN} ---------- –0.3V to 6V
- Switching Pin, LX ---------- –0.3V to 50V
- VOUT ---------- –0.3V to 46V
- Other Pins ---------- –0.3V to 6V
- Power Dissipation, P_D @ T_A = 25°C
  TSOT-23-6 ---------- 0.392W
  WDFN–8L 2x2 ---------- 0.606W
- Package Thermal Resistance (Note 3)
  TSOT-23-6, θ_{JA} ---------- 255°C/W
  WDFN–8L 2x2, θ_{JA} ---------- 165°C/W
  WDFN–8L 2x2, θ_{JC} ---------- 20°C/W
- Lead Temperature (Soldering, 10 sec.) ---------- 260°C
- Junction Temperature ---------- 150°C
- Storage Temperature Range ---------- –65°C to 150°C

Recommended Operating Conditions (Note 2)

- Junction Temperature Range ---------- –40°C to 125°C
- Ambient Temperature Range ---------- –40°C to 85°C

Electrical Characteristics

(V_{IN} = 3.7V, C_{IN} = 2.2uF, C_{OUT} = 0.47uF, I_{OUT} = 20mA, L = 22uH, T_A = 25°C, unless otherwise specified)

Parameter		Symbol	Conditions	Min	Typ	Max	Unit
Input Voltage		V_{IN}		2.5	--	5.5	V
Under Voltage Lock Out		V_{UVLO}		2	2.2	2.45	V
UVLO Hystersis				–	0.1	--	V
Quiescent Current		I_Q	FB = 1.5V, No Switching	–	400	600	uA
Supply Current		I_{IN}	FB = 0V, Switching	–	1	2	mA
Shutdown Current		I_{SHDN}	V_{EN} < 0.4V	–	1	4	uA
Line Regulation			V_{IN} = 3 to 4.3V	–	1	--	%
Load Regulation			1mA to 20mA	–	1	--	%
Operation Frequency		f_{OSC}		0.75	1	1.25	MHz
Maximum Duty Cycle				90	92	--	%
Clock Rate				0.1	--	200	kHz
Feedback Reference Voltage	RT9293A	V_{REF}		94	104	114	mV
	RT9293B			285	300	315	
	RT9293C			190	200	210	

To be continued

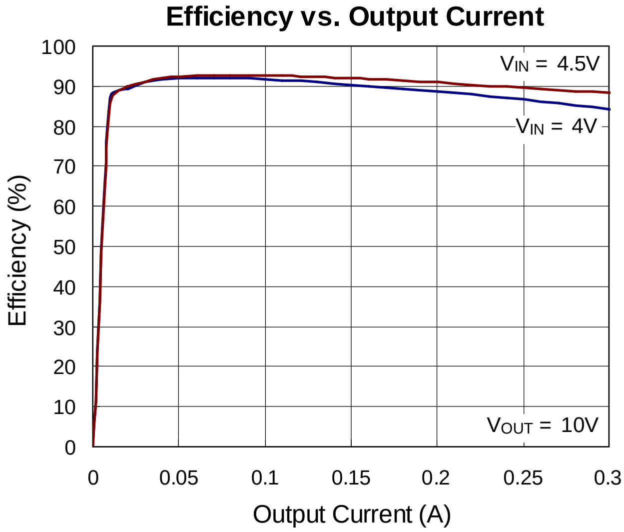
Parameter		Symbol	Conditions	Min	Typ	Max	Unit
On Resistance		$R_{DS(ON)}$		--	0.7	1.2	Ω
EN Threshold	Logic-High Voltage	V_{IH}		1.4	--	--	V
	Logic-Low Voltage	V_{IL}		--	--	0.5	V
EN Sink Current		I_{IH}		--	1	--	uA
EN Hystersis				--	0.1	--	V
Over-Voltage Threshold		V_{OVP}		42	46	50	V
Over-Current Threshold		I_{OCP}		1	1.2	--	A
OTP		T_{OTP}		--	160	--	°C
OTP Hystersis				--	30	--	°C
Shutdown Delay		T_{SHDN}		--	20	--	ms

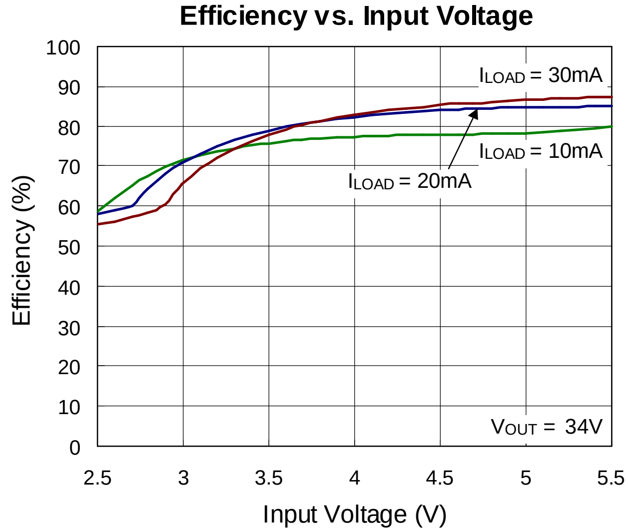
Note 1. Stresses listed as the above "Absolute Maximum Ratings" may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

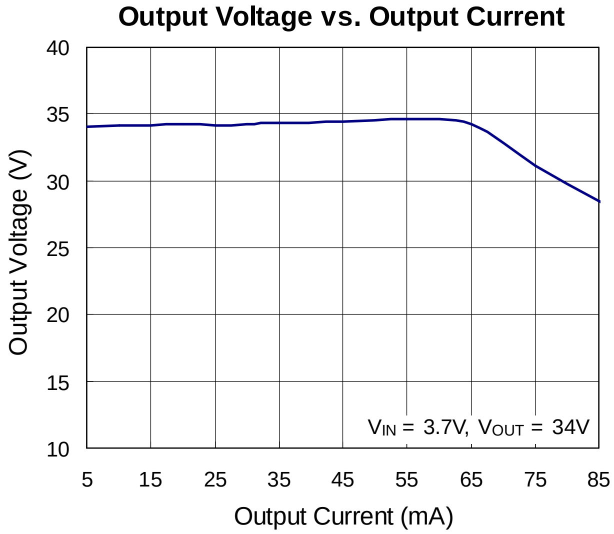
Note 2. The device is not guaranteed to function outside its operating conditions.

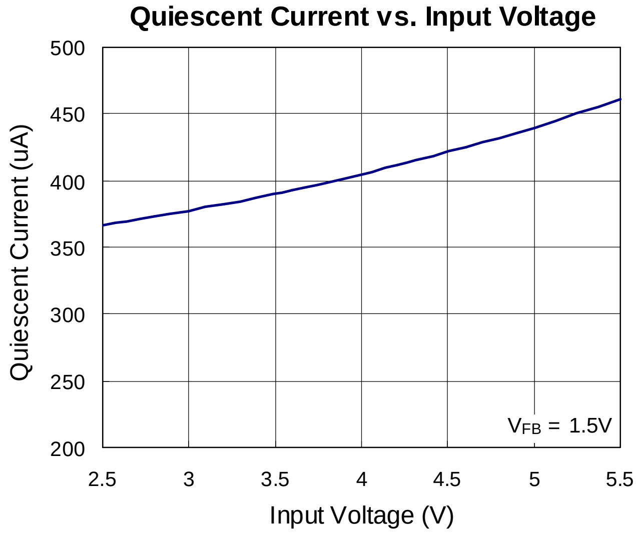
Note 3. θ_{JA} is measured in the natural convection at T_A = 25°C on a low effective single layer thermal conductivity test board of JEDEC 51-3 thermal measurement standard. The case point of θ_{JC} is on the expose pad for the WDFN package.

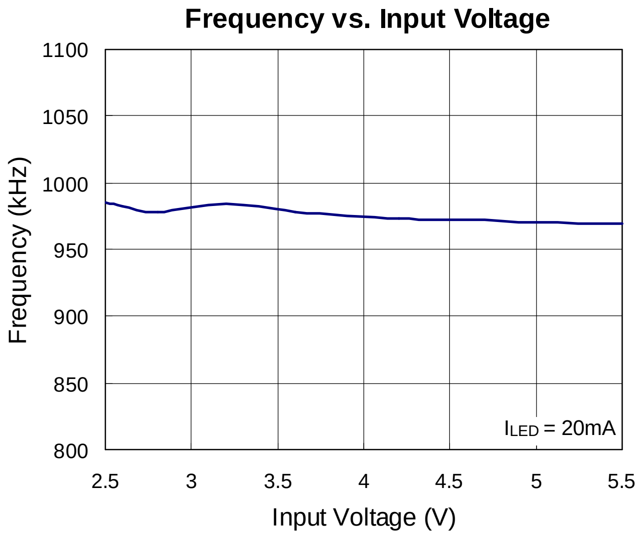
Typical Operating Characteristics

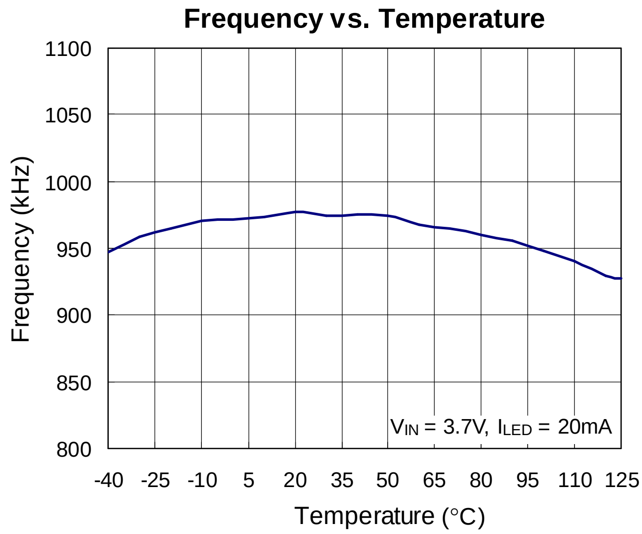


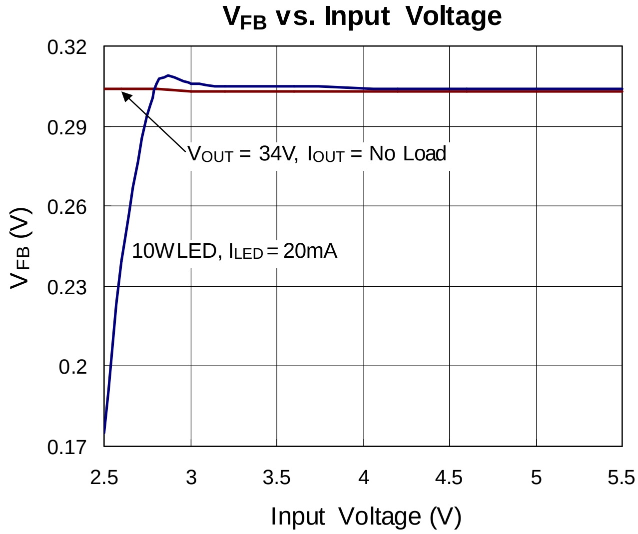


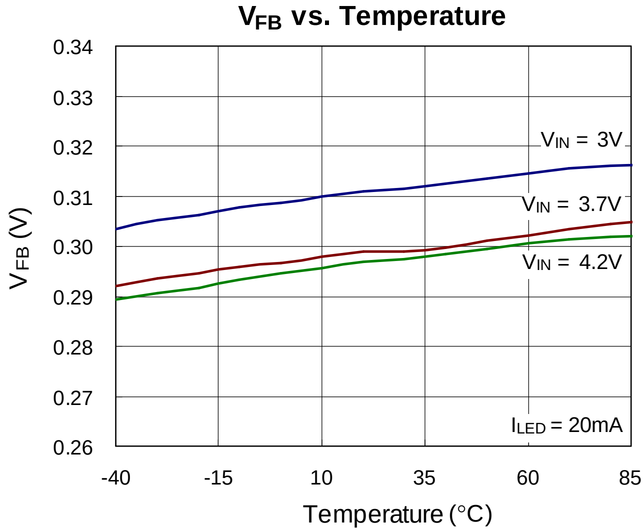


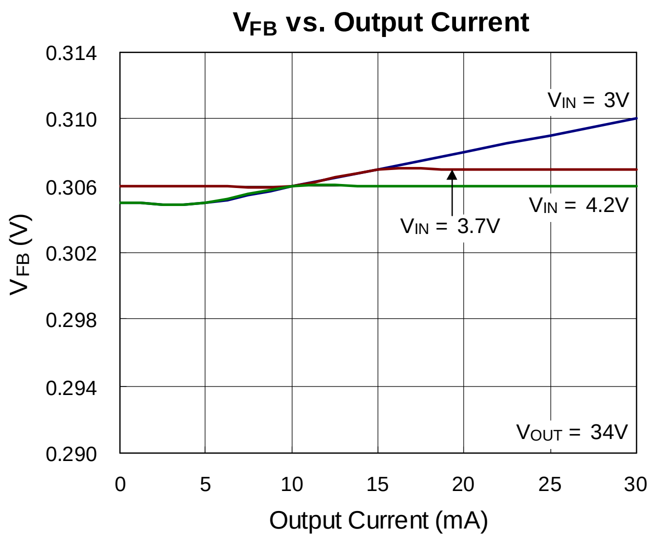


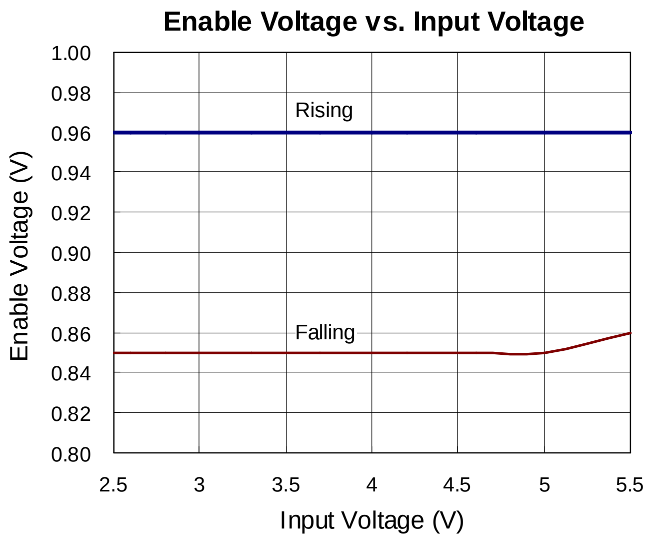


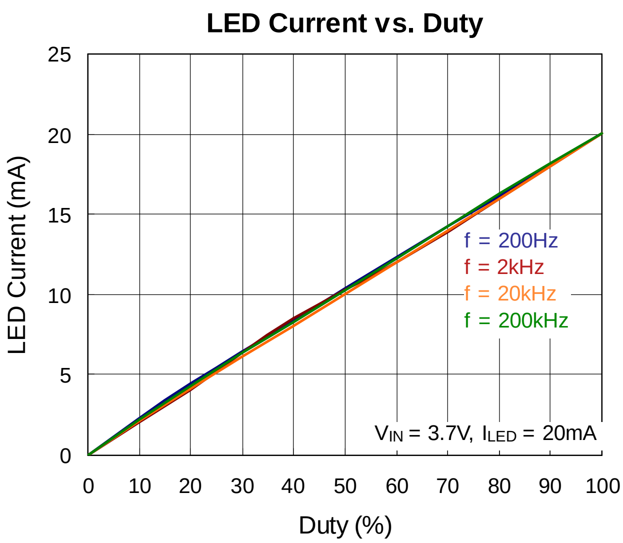


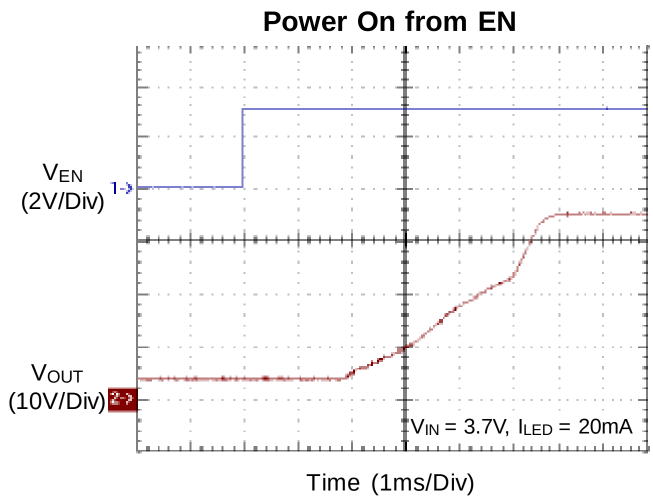

VFB vs. Input Voltage
0.32
0.29
0.26
0.23
0.2
0.17
VFB (V)
VOUT = 34V, IOUT = No Load
10WLED, ILED = 20mA
2.5
3
3.5
4
4.5
5
5.5
Input Voltage (V)


VFB vs. Temperature
0.34
0.33
0.32
0.31
0.30
0.29
0.28
0.27
0.26
VFB (V)
VIN = 3V
VIN = 3.7V
VIN = 4.2V
ILED = 20mA
-40
-15
10
35
60
85
Temperature (°C)

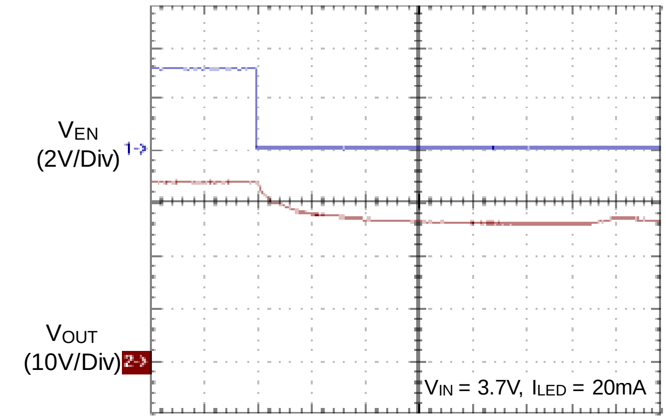

VFB vs. Output Current
0.314
0.310
0.306
0.302
0.298
0.294
0.290
VFB (V)
VIN = 3V
VIN = 4.2V
VIN = 3.7V
VOUT = 34V
0
5
10
15
20
25
30
Output Current (mA)


Enable Voltage vs. Input Voltage
1.00
0.98
0.96
0.94
0.92
0.90
0.88
0.86
0.84
0.82
0.80
Enable Voltage (V)
Rising
Falling
2.5
3
3.5
4
4.5
5
5.5
Input Voltage (V)


LED Current vs. Duty
25
20
15
10
5
0
LED Current (mA)
f = 200Hz
f = 2kHz
f = 20kHz
f = 200kHz
VIN = 3.7V, ILED = 20mA
0
10
20
30
40
50
60
70
80
90
100
Duty (%)

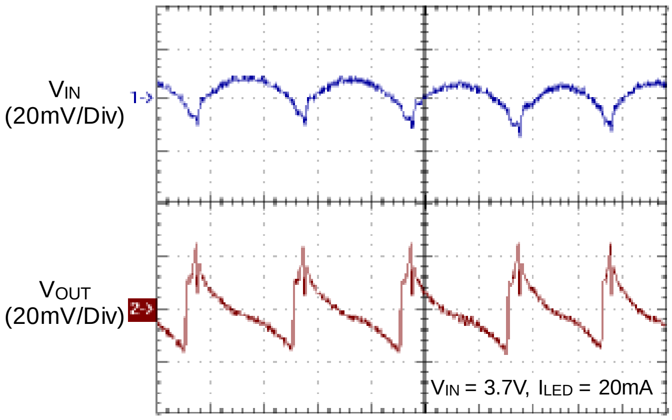

Power On from EN
VEN
(2V/Div)
VOUT
(10V/Div)
VIN = 3.7V, ILED = 20mA
Time (1ms/Div)

Power Off from EN



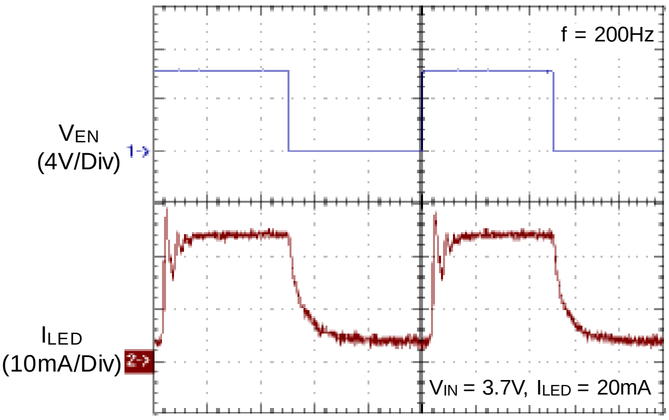
Time (1ms/Div)

Ripple



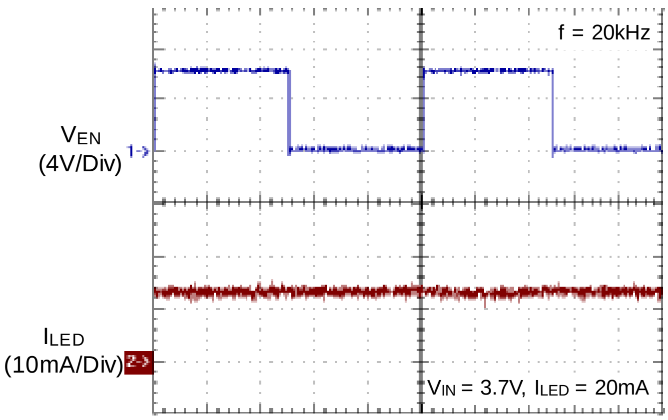
Time (500ns/Div)

PWM Dimming



Time (1ms/Div)

PWM Dimming



Time (10us/Div)

Applications Information

LED Current Setting

The loop of Boost structure will keep the FB pin voltage equal to the reference voltage V_{REF}. Therefore, when R_{REF} connects FB pin and GND, the current flows from V_{OUT} through LED and R_{REF} to GND will be decided by the current on R_{REF}, which is equal to following equation.

$$I_{LED} = \frac{V_{REF}}{R_{SET}}$$

Dimming Control

a. Using a PWM Signal to EN Pin

For controlling the LED brightness, the RT9293 can perform the dimming control by applying a PWM signal to EN pin. A low pass filter is implemented inside chip to reduce the slew rate of I_{WLED} for preventing the audio noise. The internal soft start and the wide range dimming frequency from 200 to 200kHz can eliminate inrush current and audio noise when dimming. The average LED current is proportional to the PWM signal duty cycle. The magnitude of the PWM signal should be higher than the maximum enable voltage of EN pin, in order to let the dimming control perform correctly.

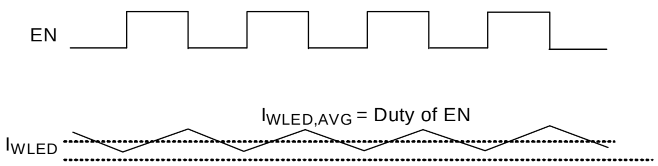


Figure 1. PWM Dimming

b. Using a DC Voltage

Using a variable DC voltage to adjust the brightness is a popular method in some applications. The dimming control using a DC voltage circuit is shown in Figure 2. As the DC voltage increases, the current pass through R3 increasingly and the voltage drop on R3 increase, i.e. the LED current decreases. For example, if the VDC range is from 0V to 2.8V and assume the RT9293B is selected which V_{REF} is equal to 0.3V, the selection of resistors in Figure 2 sets the LED current from 21mA to 0mA. The LED current can be calculated by the following equation.

$$I_{LED} = \frac{V_{REF} - \dfrac{R3 \times (V_{DC} - V_{REF})}{R4}}{R_{SET}}$$

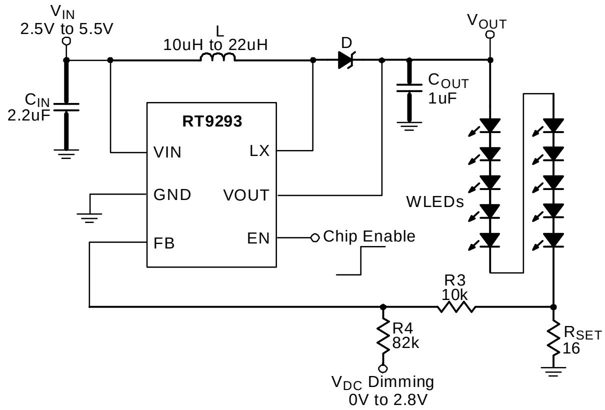


Figure 2. Dimming Control Using a DC Voltage for the RT9293

c. Using a Filtered PWM signal

Another common application is using a filtered PWM signal as an adjustable DC voltage for LED dimming control. A filtered PWM signal acts as the DC voltage to regulate the output current. The recommended application circuit is shown as Figure 3. In this circuit, the output ripple depends on the frequency of PWM signal. For smaller output voltage ripple (<100mV), the recommended frequency of 2.8V PWM signal should be above 2kHz. To fix the frequency of PWM signal and change the duty cycle of PWM signal can get different output current. The LED current can be calculated by the following equation.

$$I_{LED} = \frac{V_{REF} - \dfrac{R3 \times (V_{PWM} \times Duty - V_{REF})}{R4 + R_{DC}}}{R_{SET}}$$

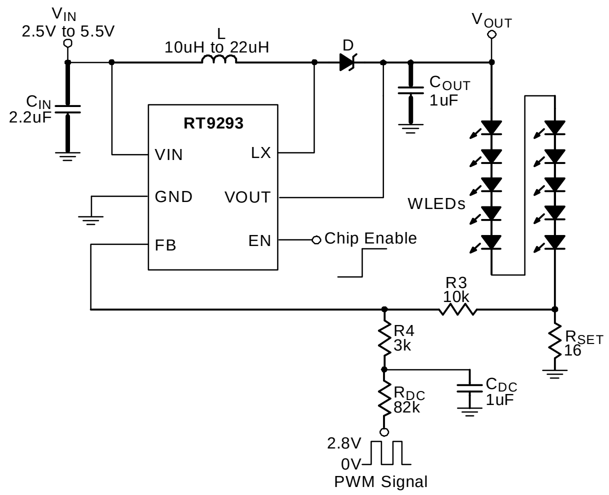


Figure 3. Filtered PWM Signal for LED Dimming Control of the RT9293

By the above equation and the application circuit shown in Figure 3, and assume the RT9293B is selected which V_{REF} is equal to 0.3V. Figure 4 shows the relationship between the LED current and PWM duty cycle. For example, when the PWM duty is equal to 60%, the LED current will be equal to 8.6mA. When the PWM duty is equal to 40%, the LED current will be equal to 12.7mA.

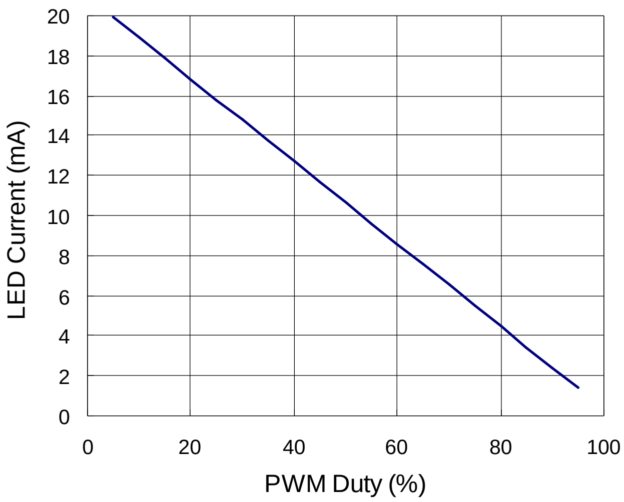


Figure 4

Constant Output Voltage Control

The output voltage of R9293 can be adjusted by the divider circuit on FB pin. Figure 5 shows the application circuit for the constant output voltage. The output voltage can be calculated by the following Equations.

$$V_{OUT} = V_{REF} \times \frac{R1+R2}{R2} \text{ ; R2 >10k}$$

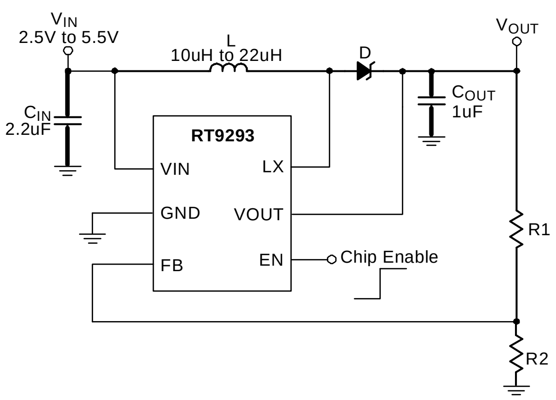


Figure 5. Application for Constant Output Voltage

Application for Driving 3 x 13 WLEDs

The RT9293 can driver different WLEDs topology. For example, the Figure 6 shows the 3x13 WLEDs and total current is equal to 260mA. The total WLEDs current can be set by the R_{REF} which is equal to following equation.

$$I_{Total} = \frac{V_{REF}}{R_{SET}}$$

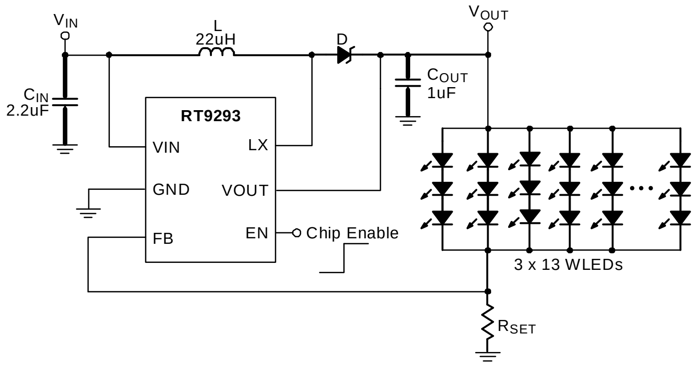


Figure 6. Application for Driving 3 X 13 WLEDs

Soft-Start

The function of soft-start is made for suppressing the inrush current to an acceptable value at the beginning of power-on. The soft-start function is built-in the RT9293 by clamping the output voltage of error amplifier so that the duty cycle of the PWM will be increased gradually in the soft-start period.

Current Limiting

The current flow through inductor as charging period is detected by a current sensing circuit. As the value comes across the current limiting threshold, the N-MOSFET will be turned off so that the inductor will be forced to leave charging stage and enter discharging stage. Therefore, the inductor current will not increase over the current limiting threshold.

OVP/UVLO/OTP

The Over Voltage Protection is detected by a junction breakdown detecting circuit. Once V_{OUT} goes over the detecting voltage, LX pin stops switching and the power N-MOSFET will be turned off. Then, the V_{OUT} will be clamped to be near V_{OVP}. As the output voltage is higher than a specified value or input voltage is lower than a specified value, the chip will enter protection mode to prevent abnormal function. As the die temperature > 160°C, the chip also will enter protection mode. The power MOSFET will be turned off during protection mode to prevent abnormal operation.

Inductor Selection

The recommended value of inductor for 10 WLEDs applications is from 10uH to 22uH. Small size and better efficiency are the major concerns for portable devices, such as the RT9293 used for mobile phone. The inductor should have low core loss at 1MHz and low DCR for better efficiency. The inductor saturation current rating should be considered to cover the inductor peak current.

Capacitor Selection

Input ceramic capacitor of 2.2uF and output ceramic capacitor of 1uF are recommended for the RT9293 applications for driving 10 series WLEDs. For better voltage filtering, ceramic capacitors with low ESR are recommended. X5R and X7R types are suitable because of their wider voltage and temperature ranges.

Thermal Considerations

For continuous operation, do not exceed absolute maximum operation junction temperature. The maximum power dissipation depends on the thermal resistance of IC package, PCB layout, the rate of surroundings airflow and temperature difference between junction to ambient. The maximum power dissipation can be calculated by following formula :

$P_{D(MAX)} = (T_{J(MAX)} - T_A) / \theta_{JA}$

Where $T_{J(MAX)}$ is the maximum operation junction temperature 125°C, T_A is the ambient temperature and the θ_{JA} is the junction to ambient thermal resistance.

For recommended operating conditions specification of RT9293, where $T_{J(MAX)}$ is the maximum junction temperature of the die (125°C) and T_A is the maximum ambient temperature. The junction to ambient thermal resistance θ_{JA} is layout dependent. For WDFN-8L 2x2 packages, the thermal resistance θ_{JA} is 165°C/W on the standard JEDEC 51-3 single layer thermal test board. The maximum power dissipation at T_A = 25°C can be calculated by following formula :

$P_{D(MAX)}$ = (125°C − 25°C) / (165°C/W) = 0.606W for WDFN-8L 2x2 packages

$P_{D(MAX)}$ = (125°C − 25°C) / (255°C/W) = 0.392W for TSOT-23-6 packages

The maximum power dissipation depends on operating ambient temperature for fixed $T_{J(MAX)}$ and thermal resistance θ_{JA}. For RT9293 packages, the Figure 7 of derating curves allows the designer to see the effect of rising ambient temperature on the maximum power allowed.

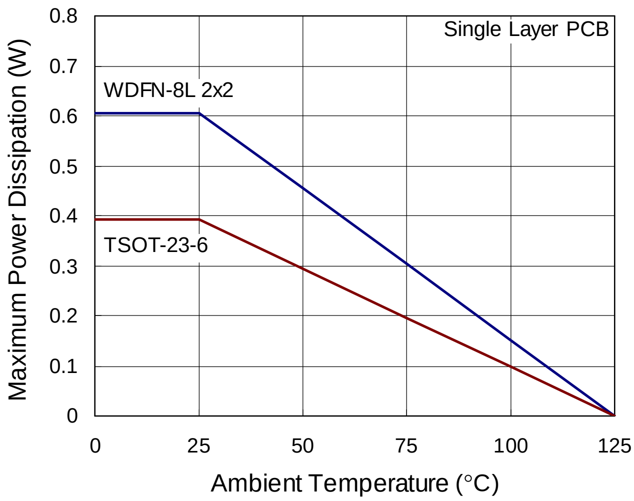


Figure 7. Derating Curves for RT9293 Packages

Layout Considerations

- A full GND plane without gap break.
- LX node copper area should be minimized for reducing EMI.
- The input capacitor C_{IN} should be placed as closed as possible to Pin 6.
- The output capacitor C_{OUT} should be connected directly from the Pin 5 to ground rather than across the LEDs.
- FB node copper area should be minimized and kept far away from noise sources (Pin 1, Pin 5, Pin 6).
- The Inductor is far away receiver and microphone.
- R_{SET} should be placed as close as possible to the RT9293.

} Traces in bold need to be routed first and should be kept as short as possible.

} VDD to GND noise bypass : Short and wide connection for the 1uF MLCC capacitor between Pin 6 and Pin 2 is recommended.

} The voice trace should be far away from the RT9293.

} The embedded antenna should be kept far away from and at different side of the RT9293.

} The through hole of the RT9293's GND pin is recommended to be large and as many as possible.

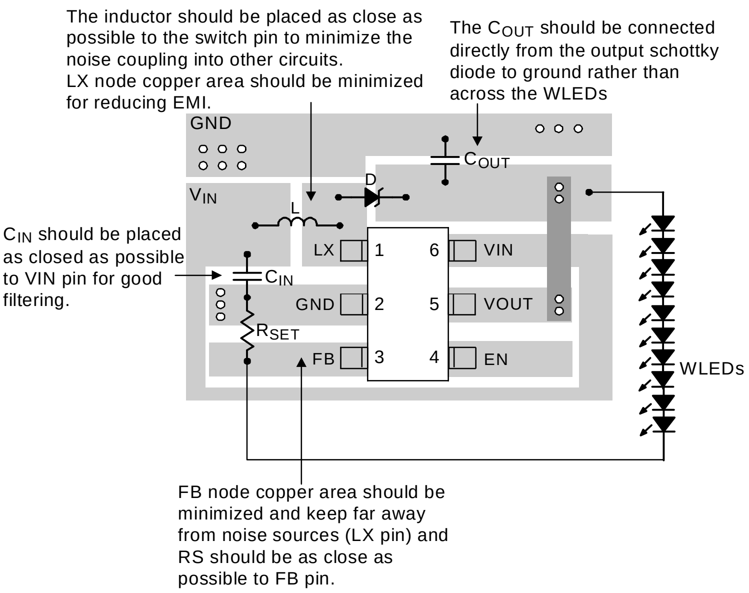
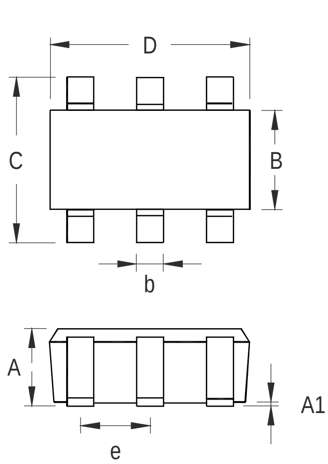


Figure 8. The Layout Consideration of the RT9293

Datasheet Revision History

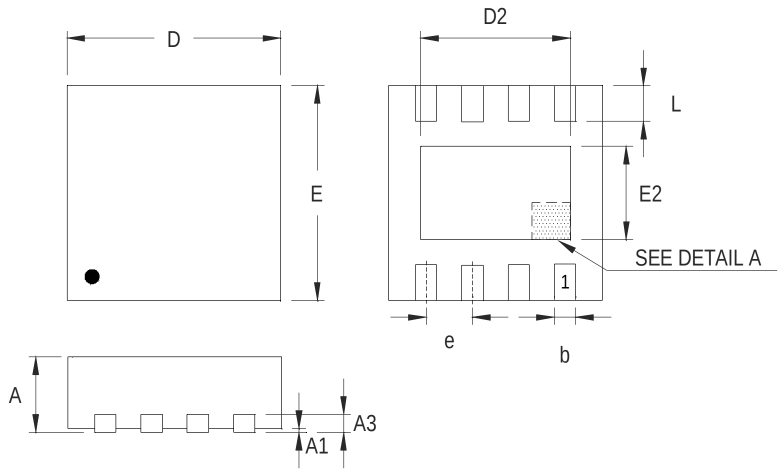
Version	Data	Page No.	Item	Description
00C	2008/1/16			First Edition
01C	2008/2/13		Headline General Description Features Absolute Maximum Ratings Recommended Operating Conditions Electrical Characteristics	Modify
02C	2008/3/18		General Description Ordering Information Typical Application Circuit Electrical Characteristics	Change from RT9293A/B to RT9293 Modify. Previous RT9293 Phase Out_by Eric/PME
03C	2008/4/10		Typical Application Circuit Absolute Maximum Ratings Typical Operating Characteristics Applications Information	Modify Add Typical Operating Characteristics and Applications Information

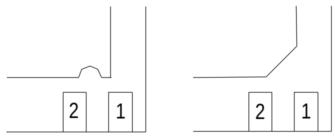
Outline Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.700	1.000	0.028	0.039
A1	0.000	0.100	0.000	0.004
B	1.397	1.803	0.055	0.071
b	0.300	0.559	0.012	0.022
C	2.591	3.000	0.102	0.118
D	2.692	3.099	0.106	0.122
e	0.838	1.041	0.033	0.041
H	0.080	0.254	0.003	0.010
L	0.300	0.610	0.012	0.024

TSOT-23-6 Surface Mount Package





DETAIL A

Pin #1 ID and Tie Bar Mark Options

Note : The configuration of the Pin #1 identifier is optional, but must be located within the zone indicated.

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A3	0.175	0.250	0.007	0.010
b	0.200	0.300	0.008	0.012
D	1.950	2.050	0.077	0.081
D2	1.000	1.250	0.039	0.049
E	1.950	2.050	0.077	0.081
E2	0.400	0.650	0.016	0.026
e	0.500		0.020	
L	0.300	0.400	0.012	0.016

W-Type 8L DFN 2x2 Package

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